

Scheme and Syllabus of III Year UG Programme

SCHEME OF TEACHING AND EXAMINATION V SEMESTER							
Sl. No.	Subject Code	Subject	Category	L	T	P	Cr.
1	EC5C01	Digital Signal Processing	Department core	3	2	0	4
2	EC5C02	Digital Design using Verilog	Department core	3	2	0	4
3	EC5C03	Analog and Digital Communication	Department core	4	0	0	4
4	EC5C04	Control Systems	Department core	3	0	0	3
5	EC5MXX	MOOC Elective	MOOC Elective	3	0	0	3
6	EC5C06	Engineering Management	Management	3	0	0	3
7	EC5L01	Communication Laboratory	Laboratory	0	0	3	1.5
8	EC5L02	Digital Signal Processing Laboratory	Laboratory	0	0	3	1.5
Total Credits							24
Total Contact Hrs							29
9	EC5M01	Joy of computing with python	MOOC Elective	3	0	0	3
10	EC5M02	Digital VLSI testing		3	0	0	3
11	EC5V01	Robotics	Value Added Course	-	-	-	-
12	EC5V02	Scientific computing using Matlab		-	-	-	-
13	EC5V03	Managerial economics		-	-	-	-
14	EC5V04	Linear algebra		-	-	-	-

SCHEME OF TEACHING AND EXAMINATION VI SEMESTER							
Sl. No.	Subject Code	Subject	Category	L	T	P	Cr.
1	EC6C01	CMOS VLSI Design	Department core	3	0	0	3
2	EC6C02	Communication systems and coding theory	Department core	3	0	0	3
3	EC6C03	Communication Networks	Department core	3	0	0	3
4	EC6C04	Embedded Systems	Department core	3	2	0	4
5	EC6C05	Engineering Economics	Economics	3	0	0	3
6	EC6E1XX	Department Elective - 1	Department Elective	3	0	0	3
7	EC6L01	Communication Networks Laboratory	Laboratory	0	0	3	1.5
8	EC6L02	CMOS VLSI Laboratory	Laboratory	0	0	3	1.5

9	EC6C06	Minor Project	Project	0	0	-	2
Total Credits							24
Total Contact Hrs							26
1	EC6E101	Bioinformatics	Department Elective - 1	3	0	0	3
2	EC6E102	Digital Image Processing		3	0	0	3
3	EC6E103	Neural Networks		3	0	0	3
4	EC6E104	Internet of Things		2	0	2	3